

ABSTRACT OF THE DISCLOSURE

A panel of a flat panel display comprises a glass substrate, switches, photoresist layers, signal lines disposed on the glass substrate along a first direction, and gate lines disposed on the glass substrate along a second direction and across the first direction to define pixels. Each pixel includes a first area. A plurality of switching units are formed in the first areas of the pixels to control the corresponding pixels. The first, second, and third photoresist layers are disposed in the first, second, and third groups of the pixels, the each first area of each pixel is covered by at least two of the first, second, and third photoresist layers.